



Micro Commercial Components



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BSS84

Features

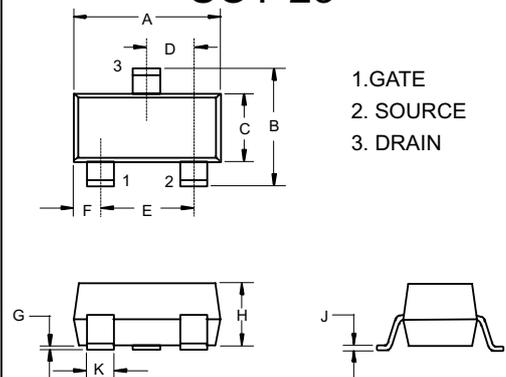
- High dense cell design for extremely low $R_{DS(ON)}$
- Rugged and reliable
- Lead free product is acquired
- SOT-23 Package
- Marking Code: B84
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1

Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Parameter	Rating	Unit
V_{DS}	Drain-source Voltage	-50	V
I_D	Drain Current-Continuous	-0.13	A
I_{DM}	Drain Current-Pulsed	-0.52	A
V_{GS}	Gate-source Voltage	± 20	V
P_D	Total Power Dissipation	225	mW
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	556	$^{\circ}C/W$
T_J	Operating Junction Temperature	-55 to +150	$^{\circ}C$
T_{STG}	Storage Temperature	-55 to +150	$^{\circ}C$

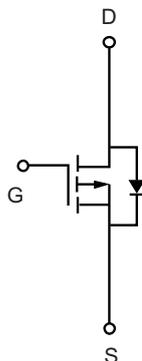
P-Channel Enhancement Mode Field Effect Transistor

SOT-23

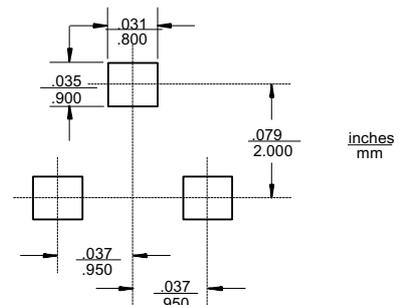


DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.110	.120	2.80	3.04	
B	.083	.104	2.10	2.64	
C	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
E	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
H	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	

Internal Block Diagram



Suggested Solder Pad Layout



BSS84

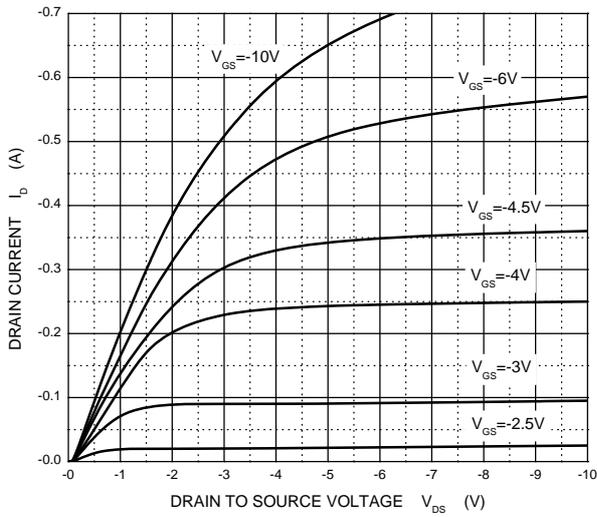
Electrical characteristics per line @25°C (unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
STATIC CHARACTERISTICS						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$	-50			V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -50V, V_{GS} = 0V$			-15	μA
		$V_{DS} = -25V, V_{GS} = 0V$			-0.1	μA
Gate-body leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 5	μA
Gate threshold voltage (note 3)	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-0.9		-2	V
Drain-source on-resistance (note 3)	$R_{DS(on)}$	$V_{GS} = -5V, I_D = -0.1A$			10	Ω
		$V_{GS} = -10V, I_D = -0.1A$			8	Ω
Forward transconductance (note 1)	g_{FS}	$V_{DS} = -25V; I_D = -100mA$	50			mS
DYNAMIC CHARACTERISTICS (note 4)						
Input capacitance	C_{iss}	$V_{DS} = 5V, V_{GS} = 0V, f = 1MHz$		30		pF
Output capacitance	C_{oss}			10		pF
Reverse transfer capacitance	C_{rss}			5		pF
SWITCHING CHARACTERISTICS (note 4)						
Turn-on delay time	$t_{d(on)}$	$V_{DD} = -15V,$ $R_L = 50\Omega, I_D = -2.5A$		2.5		ns
Turn-on rise time	t_r			1		ns
Turn-off delay time	$t_{d(off)}$			16		ns
Turn-off fall time	t_f			8		ns
SOURCE-DRAIN DIODE CHARACTERISTICS						
Continuous Current	I_S	$I_S = -0.13A, V_{GS} = 0V$			-0.13	A
Pulsed Current	I_{SM}				-0.52	A
Diode forward voltage (note 3)	V_{SD}				-2.2	V

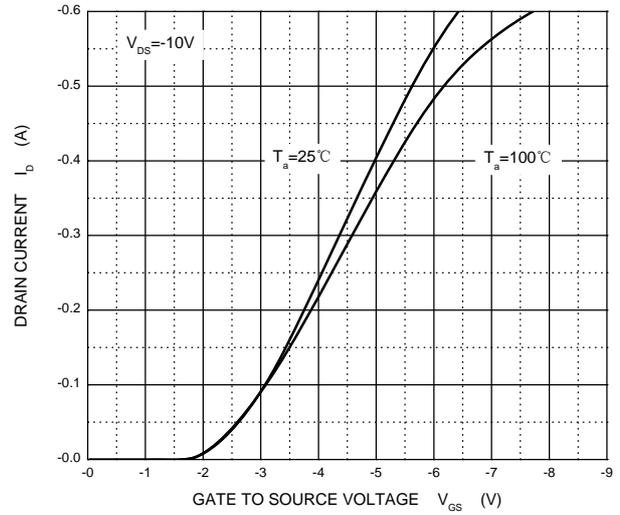
Notes :

1. Repetitive rating : Pulse width limited by junction temperature.
2. Surface mounted on FR4 board , $t \leq 10s$.
3. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycles $\leq 2\%$.
4. Guaranteed by design, not subject to producing.

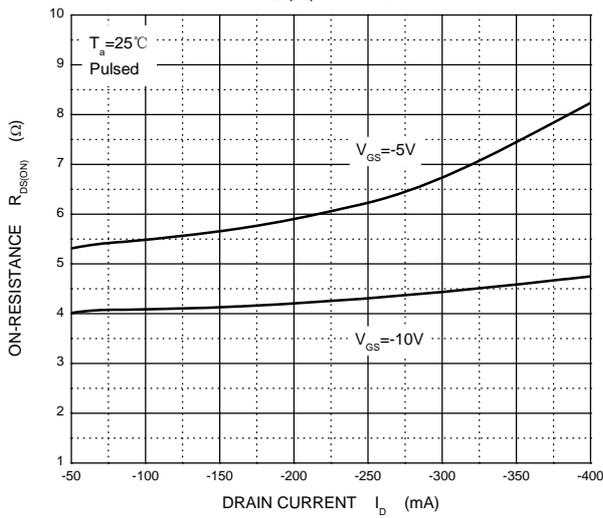
Output Characteristics



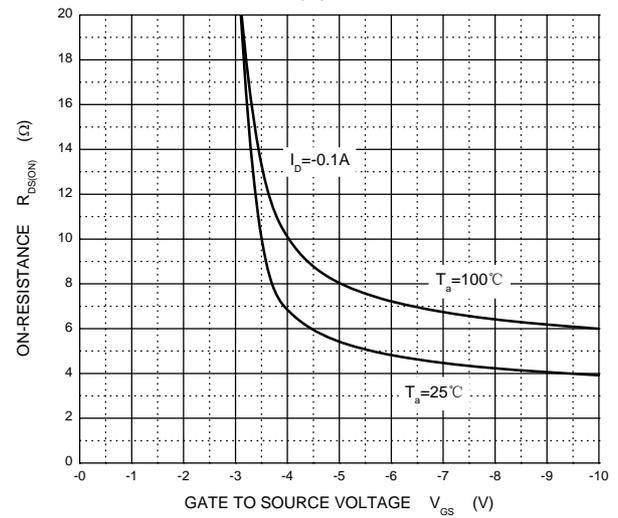
Transfer Characteristics



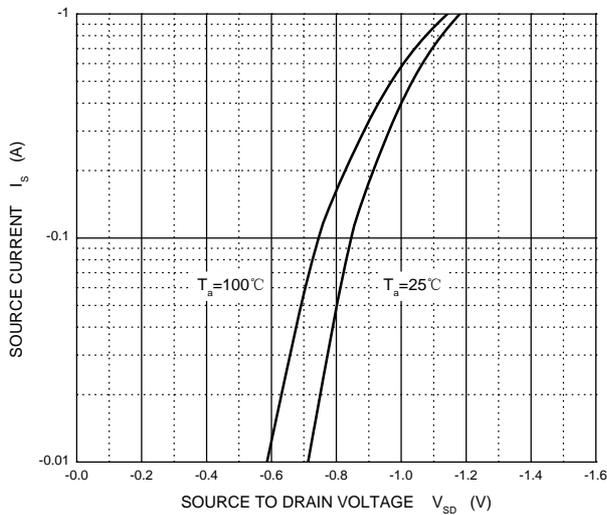
$R_{DS(ON)}$ — I_D



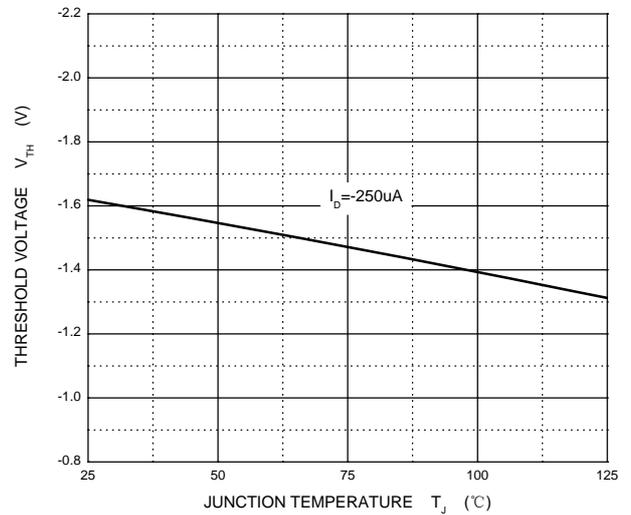
$R_{DS(ON)}$ — V_{GS}



I_S — V_{SD}



Threshold Voltage





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Ordering Information :

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

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